



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gerd Frankowsky et al.

Art Unit: 2811

Examiner: Joseph H Nguyen

Serial No.: 10/630,632

Filed

Title

: July 29, 2003

: SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING

SEMICONDUCTOR CIRCUIT MODULES

Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO RESTRICTION REQUIREMENT OF DECEMBER 3, 2004

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 13, 2004 Date of Deposit

Signature

Matthew G. Doherty

Typed or Printed Name of Person Signing Certificate